PATENT NUMBER

## U.S. **UTILITY** Patent Application

| | O.I.P.E. | PATENT DATE |
|---|---|---|
| SCANNED | Q.A. | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/511526 | | 379 370 | 442 | 2743 2742 2729 2665 | Philpott, J. |

APPLICANTS: Barry Jones

TITLE: Timeslot interchange circuit supporting pcm, adpcm, and multiple data channel connectivity to t1 and e1 circuits

PTO-2040
12/99

## ISSUING CLASSIFICATION

| ORIGINAL | | | CROSS REFERENCE(S) | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | |
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☐ Continued on Issue Slip Inside File Jacket

| ☐ TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets.Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | (Assistant Examiner)          (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| | (Primary Examiner)          (Date) | | | ISSUE BATCH NUMBER | |
| ☐ The terminal ____months of this patent have been disclaimed. | (Legal Instruments Examiner)          (Date) | | | | |

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